

ABSTRACT OF THE DISCLOSURE

Disclosed herein is a wafer level package for a micro device, such as an FBAR device. The wafer level package 5 comprises a device wafer having a micro device, and bonding pads connected to the micro device, which are formed at one surface of the device wafer, via connectors extending from the bonding pads to the other surface of the device wafer, external bonding pads formed at the other surface of the 10 device wafer and adapted to be connected to the bonding pads through the via connectors, respectively, and a cap structure bonded to one surface of the device wafer so as to allow the micro device to be insulated and hermetically sealed.

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